

LSI LOGIC

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LSI Logic Corporation  
Legal Department - IP  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035Date March 8, 2006Number of pages including cover sheet 2

To: US PTO

Mail Stop Status Request

TC 2138

Fax No. 571-273-8300

Phone No.

CC:

From: Connie del Castillo

Intellectual Property Paralegal

Telephone (408) 433-7191  
No.

Fax No. (408) 433-7460

## REMARKS:

☐ Urgent ☐ For your review ☒ Reply ASAP ☐ Please commentApplication Number: 10/701,332  
Filing date: November 4, 2003  
First named inventor: Mukesh Puri  
Attorney docket number: 03-1343

Transmitted herewith for filing via facsimile:

- Status Request Letter

Pursuant to 37 C.F.R. 1.8, I hereby certify that this correspondence is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date indicated below:

3/8/06

Date

Connie del Castillo

Signature

Connie del Castillo

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of : Mukesh Puri

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Serial No.: 10/701,332

Group Art Unit: 2138

**MAR 08 2006**

Filed: 11/4/2003

For: Novel BISR Mode to Test the  
Redundant Elements and Regular  
Functional Memory to Avoid Test  
Escapes

Atty Docket: 03-1343

**STATUS REPORT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

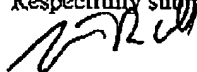
Sir:

Applicants request a status of the above referenced application. Please mail the status notification to the address stated below.

LSI Logic Corporation  
1621 Barber Lane, MS D-106  
Milpitas, CA 95035  
408-433-7475

Date: 8 MAR 06

Respectfully submitted,



Timothy Croll, Ph: [+1] 408-433-7191

Reg. No. 36,771